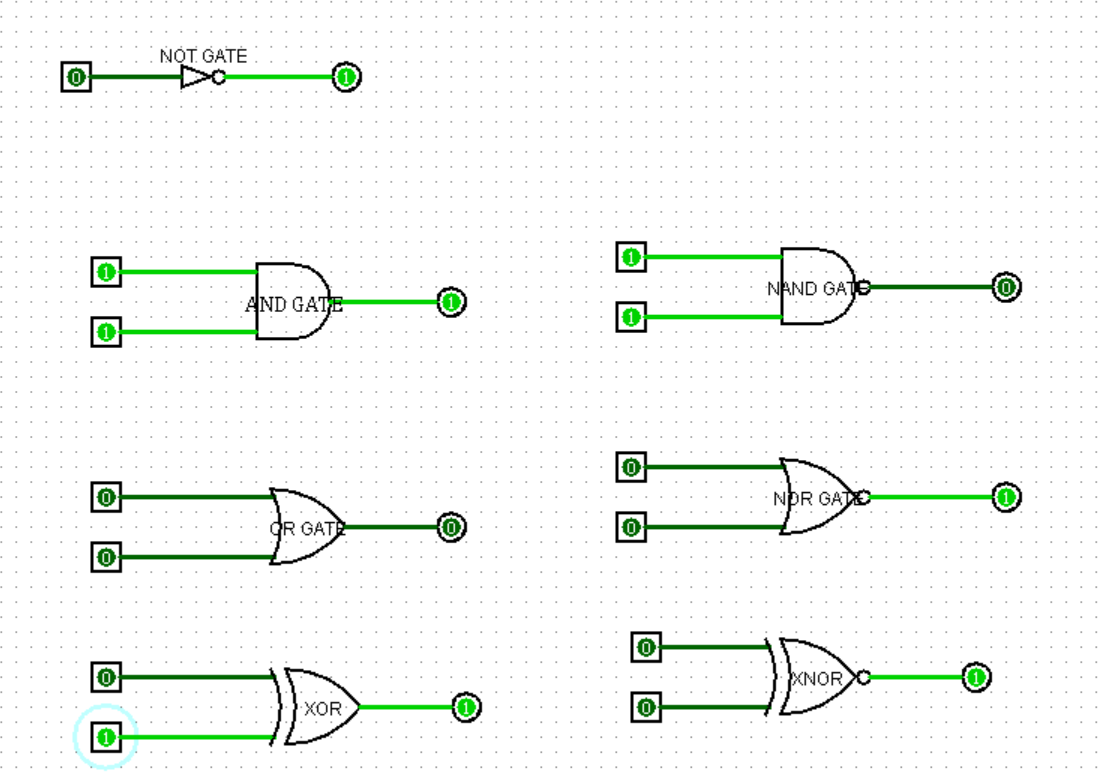
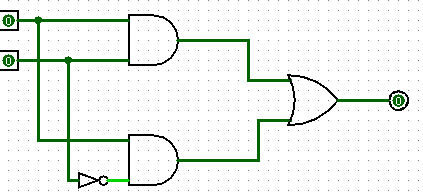
Practical 1: logic gates

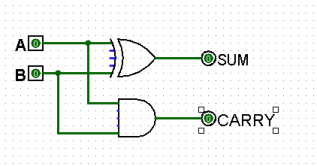


Practical 2: Boolean expression

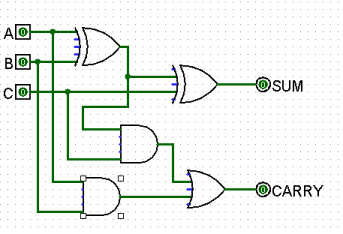


Practical 3:

Half adder



Full adder



Practical 4:

Half subtractor

A diagram of a circuit

AI-generated content may be incorrect.

Full subtractor

A diagram of a circuit

AI-generated content may be incorrect.

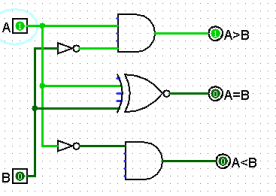
Practical 5:

4-bit magnitude

A diagram of a circuit

AI-generated content may be incorrect.

1 -bit magnitude



Practical 6: flip flop

SR

A diagram of a circuit

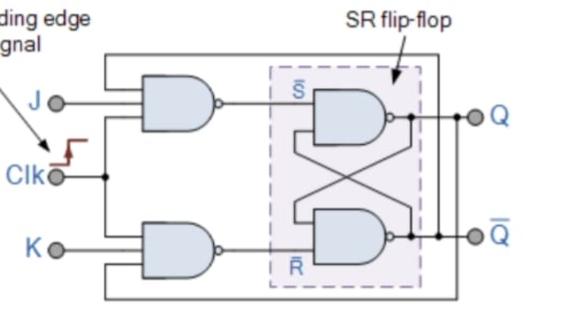
AI-generated content may be incorrect.

D flip flop

A diagram of a block diagram

AI-generated content may be incorrect.

JK



Practical 7: synchronous and asynchronous

Synchronous

A diagram of a block diagram

AI-generated content may be incorrect.

Asynchronous

A diagram of a diagram

AI-generated content may be incorrect.

Practical 8:shift register

A diagram of a network

AI-generated content may be incorrect.

Practical 9:

Multiplexer

A screenshot of a computer

AI-generated content may be incorrect.

Demultiplex

A computer screen shot of a computer network

AI-generated content may be incorrect.